

INFORMATION DISCLOSURE CITATION

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Applicant	Tuung Luoh	APR 12 2004 JN	
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U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate
P D	5,981,339	November 9, 1999	Chang et al.	—	—	—
P D	6,383,899	May 7, 2002	Voutsas	—	—	—
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FOREIGN PATENT DOCUMENTS

	Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

P D	T.C. Ong, et al., "Erratic Erase in ETOX™ Flash Memory Array," IEEE VLSI Symposium, 1993, pp. 83-84.
P D	V. Subramanian et al., "A Novel Technique for 3-D Integration: Ge-seeded Laterally Crystallized TFTs", IEEE VLSI Symposium, 1997.
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Examiner	PHUE T. DANG	Date Considered	12/8/2004
*Examiner:	Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		
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